



122 100 -M_SEL K:1 30-120 27A -128 IDLEP 20A 124 Q85 Q41 REF_POS 126 T_DATA Q27 Q75 T_INH ์ Q30 26 Q29 R18≸ VEE VCC Q4 VCC Q5 R42 **≷** Q56 Q55 ó OUT R40 **≷** VEE **⋛** R43 Q54 Q4 Ϋ́ΕΕ Q46 VCC Q L Q52 R15 **≷** 24B Q31 B_INH 28 Q32 B_DAŢA **Q74** Q28 22B REF_NEG Q42B Q42A 20B 132 110-**IDLEN** M_SEL K:1 130 30-138 FIG. 2



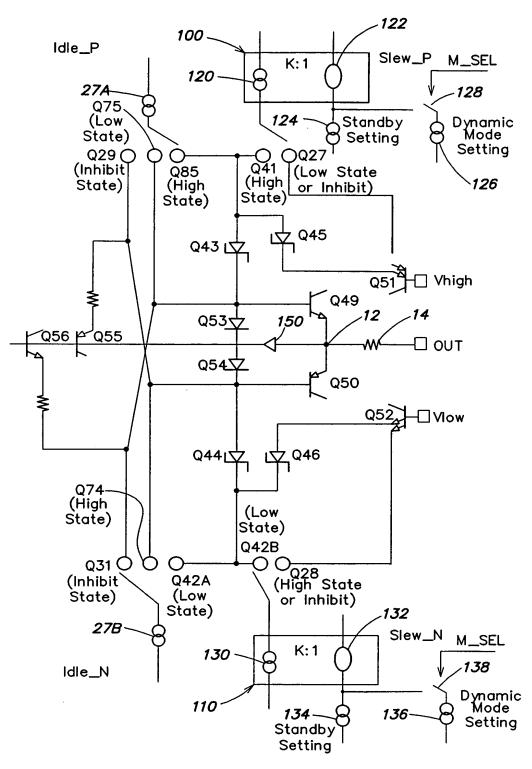


FIG. 3



Driver Mode	Driver State	Slew Current	High—Side Transistors On	Low-Side Transistors On
Dynamic	Low (Active)	Dynamic + Standby	Q75, Q27	Q42A, Q42B
Dynamic	High (Active)	Dynamic + Standby	Q85, Q41	Q74, Q28
Dynamic	Inhibit	Dynamic + Standby	Q29, Q27	Q31, Q28
Termination	Low (Active)	Standby	Q75, Q27	Q42A, Q42B
Termination	High (Active)	Standby	Q85, Q41	074, 028
Termination	Inhibit	Standby	Q29, Q27	Q31, Q28

F1G. 4